U.S. DEPT. OF COMMERCE PATENT AND TRADEMARK OFFICE

Form PTO-1449 **LIST OF PRIOR ART CITED BY APPLICANT** *(Use several sheets if necessary)*	ATTORNEY DOCKET NO. **AUS920010565US1**	SERIAL NO. **09/925,584**
	APPLICANT **Austen et al.**	
	FILING DATE **August 9, 2001**	GROUP ART UNIT **2182**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.		PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
SKS	AA	4564903	Jan. 14, 1986	Guyette et al.	364/300	Oct. 5, 1983
SKS	AB	4843541	Jun. 27, 1989	Bean et al.	364/200	Jul. 29, 1987
SbS	AC	5345590	Sep. 6, 1994	Ault et al.	395/650	Sep. 1, 1993

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO

OTHER PRIOR ART *(including author, title, date, pertinent page, etc.)*

SbS	AD	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Hypervisor High Performance Synchronous Dispatch for Coupled Systems", one page.
SbS	AE	IBM Technical Disclosure Bulletin, Vol. 38, No. 04, April 1995, "VM MPG Operating as a DRF Hypervisor as a First Level Guest Under PR/SM", p. 325.
SbS	AF	IBM Technical Disclosure Bulletin, Vol. 36, No. 03, March 1993, "Sharing Read-Only Memory among Multiple Logical Partitions", pp. 303-304.
SbS	AG	IBM Technical Disclosure Bulletin, Vol. 39, No. 12, December 1996, "Highly Parallel Coupling Facility Emulator/Router with Shadowed Link Buffers", 2 pages.
SbS	AH	IBM Technical Disclosure Bulletin, Vol. 39, No. 06, June 1996, "Coordinating Multiple Server Partitions to Enter Power-Save State", pp. 235-239.

DATE CONSIDERED 7/9/04	EXAMINER *Suresh K Suryawanshi*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

0001RF-108/49776.1

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